

L Number	Hits	Search Text	DB	Time stamp
1	135	714/739.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/02 14:31
2	4	(714/739.ccls.) and ((random adj pattern) with bit\$1 with generator) and parallel and serial and compar\$5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/02 14:39
3	3	(714/739.ccls.) and ((random adj pattern) with bit\$1 with generator) and parallel and serial and compar\$5 and (((integrated adj circuit) or IC) with test\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/02 14:41
4	0	(714/739.ccls.) and ((random adj pattern) with bit\$1 with generator) and parallel and serial and compar\$5 and (((integrated adj circuit) or IC) with test\$3) and serializer and deserializer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/02 14:43
5	0	(714/715.ccls.) and ((random adj pattern) with bit\$1 with generator) and parallel and serial and compar\$5 and (((integrated adj circuit) or IC) with test\$3) and serializer and deserializer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/02 14:43
6	0	(714/716.ccls.) and ((random adj pattern) with bit\$1 with generator) and parallel and serial and compar\$5 and (((integrated adj circuit) or IC) with test\$3) and serializer and deserializer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/02 14:43
7	0	((random adj pattern) with bit\$1 with generator) and parallel and serial and compar\$5 and (((integrated adj circuit) or IC) with test\$3) and serializer and deserializer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/02 14:44
8	0	((random adj pattern) with bit\$1 with generator) and parallel and serial and compar\$5 and (((integrated adj circuit) or IC) with test\$3) and serializer and de-serializer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/02 14:44
9	0	((random adj pattern) with bit\$1 with generator) and parallel and serial and compar\$5 and (((integrated adj circuit) or IC) with test\$3) and serializer and (de adj serializer)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/02 14:44
10	0	((random adj pattern) with bit\$1 with generator) and parallel and serial and compar\$5 and (((integrated adj circuit) or IC) with test\$3) and serializer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/02 14:45
11	0	(714/715.ccls.) and ((random adj pattern) with bit\$1 with generator) and parallel and serial and compar\$5 and (((integrated adj circuit) or IC) with test\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/02 14:45
12	0	(714/716.ccls.) and ((random adj pattern) with bit\$1 with generator) and parallel and serial and compar\$5 and (((integrated adj circuit) or IC) with test\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/02 14:45
13	10	((random adj pattern) with bit\$1 with generator) and parallel and serial and compar\$5 and (((integrated adj circuit) or IC) with test\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/02 14:47
14	1228	serializer and deserializer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/02 14:48

15	52	serializer and deserializer and (((integrated adj circuit) or IC) with test\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/02 14:49
16	0	serializer and deserializer and (((integrated adj circuit) or IC) with test\$3) and ((random adj pattern adj generator) with bit\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/02 14:50
17	0	serializer and deserializer and (((integrated adj circuit) or IC) with test\$3) and (random adj pattern adj generator\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/02 14:50
18	21	serializer and deserializer and (((integrated adj circuit) or IC) with test\$3) and (random with generator\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/02 14:50
19	13	serializer and deserializer and (((integrated adj circuit) or IC) with test\$3) and (random with generator\$1 with bit\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/02 14:51
21	11	serializer and deserializer and (((integrated adj circuit) or IC) with test\$3) and (random with generator\$1 with bit\$1) and compar\$5 and parallel	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/02 14:51
22	11	serializer and deserializer and (((integrated adj circuit) or IC) with test\$3) and (random with generator\$1 with bit\$1) and compar\$5 and parallel and serial	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/02 16:24
23	11	((second adj random) with generator with pattern) and compar\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/02 16:27
24	3	((second adj random adj pattern adj generator) and compar\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/02 16:30
25	44	((random adj pattern adj generators) and compar\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/02 16:32
26	1	((random adj pattern adj generators) with identical with bits	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/02 16:32
20	11	serializer and deserializer and (((integrated adj circuit) or IC) with test\$3) and (random with generator\$1 with bit\$1) and compar\$5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/02 17:22
27	4914	latch with output with signal with stor\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/02 17:23
28	1	((latch with output with signal with stor\$3) and compar\$5 with serializer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/02 17:24

29	2	(output adj latch) with signal with stor\$3 with fail\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/02 17:26
30	620	(output adj latch) with signal with stor\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/02 17:26
31	25	(output adj latch) with (stor\$3 adj signal)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/02 17:32
32	3	((output adj latch) with (stor\$3 adj signal)) and serial and parallel and communication	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/02 18:48
33	19	STAPL	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/02 18:48
34	1	STAPL and JAM	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/02 18:49
35	0	JAM with (standard adj test adj programming adj language)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/02 18:50
36	0	JAM same (standard adj test adj programming adj language)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/02 18:50
37	258	JAM same programming	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/02 18:50
38	12	JAM same (programming adj language)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/02 19:59
39	154935	printed adj circuit adj board	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/02 20:00
40	18	(printed adj circuit adj board) with serializer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/02 20:00
41	7	(printed adj circuit adj board) with serializer with deserializer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/02 20:00
42	5	(printed adj circuit adj board) with serializer with deserializer with comparator	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/02 20:00